



10° Workshop on Automotive Software & Systems

Milano – 25 Ottobre 2012

Hotel Four Points by Sheraton, via Cardano,1 (zona Stazione Centrale)

Automotive SPICE e strategie efficaci di V&V in contesti progettuali medio-piccoli

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SPICE

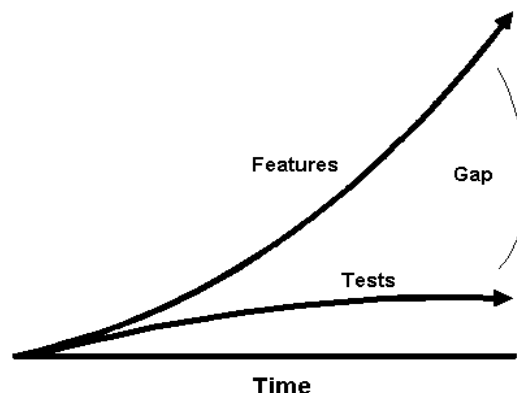
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What is about?

This presentation is an excursus along V&V in the context of ECU SW Projects in the "small settings" arena.

- ❖ ***The processes related to V&V are the ones definitively more challenging for such a kind of organizations.***



The aim is at presenting a set of V&V approaches as a valuable trade-off between Automotive SPICE requirements and a sustainable and effective V&V implementation for an average "small setting" project.

What is NOT about?

Definition of V&V Strategies intrinsically compliant with Automotive SPICE.

- ❖ ***Strategies needs to be adequate to the context!***
- ❖ ***Strategies needs to be well detailed and deployed correctly and systematically!***



The ideas behind this presentation emerge from author working experiences at Suppliers of small-to-medium ECUs.

- ❖ ***Focus on ECUs based on 8 bit and 16 bit microcontrollers***
- ❖ ***Software is traditionally hand-written (i.e. no Model Based development)***

In "Small Settings" there are well-known reasons why Automotive SPICE is so hard to be applied, above all:

- ❖ ***High Costs of establishing the necessary infrastructure***
- ❖ ***Large number of roles which must be filled by a restricted number of staff***
- ❖ ***Amount of information that must be assimilated to properly interpret the Automotive SPICE model***

The “Small Setting” Context

OEMs typically require ECU suppliers to apply the **same SW Standards** regardless the size of the project and of the ECU Supplier.

What are these standards typically?

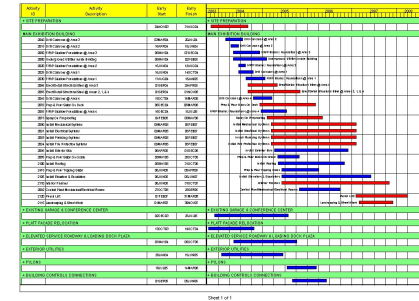
- ❖ ***MISRA C 2004***
- ❖ ***Automotive SPICE***
- ❖ ***ISO 26262 (if functional safety comes into picture)***

.... and it sometimes does!

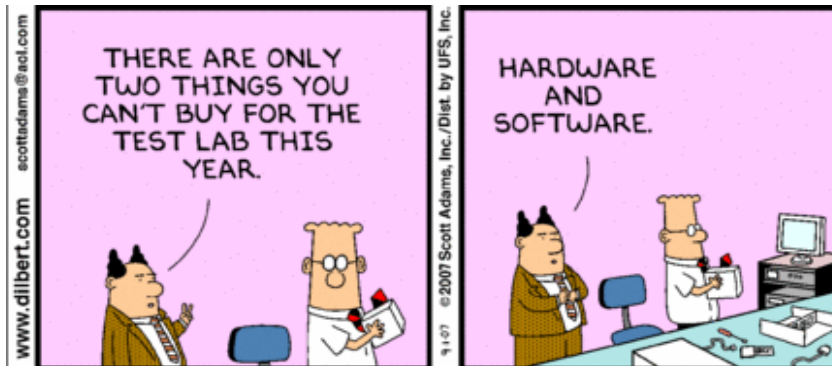
The "Small Setting" Context (2)

... and since we are in Automotive:

❖ **Schedules are always compressed**
and test windows get squeezed!!!



❖ **Not much room for investments (man-power, tools, automation ...)**

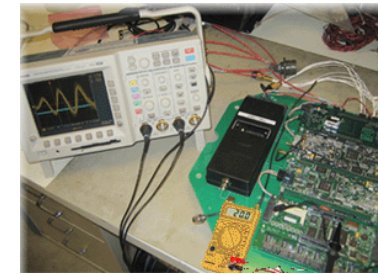


The "Small Setting" Context (3)

What is a typical scenario in a "small setting" project?

1 or 2 SW Engineers developing the project

They typically develop and test/debug the SW on Target and Emulator.



1 System Tester that tests the whole system

He/She tests heavily focuses on Customer requirements and applicable norms.



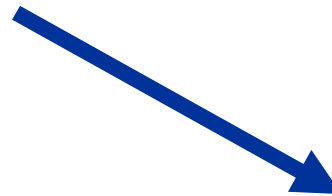
V&V in Automotive SPICE

Automotive SPICE Engineering Process Areas with V&V relevant content:

- ❖ ***ENG.6 – SW Costruction***
- ❖ ***ENG.7 - SW Integration Testing***
- ❖ ***ENG.8 - SW Testing***
- ❖ ***ENG.9 - System Integration Testing***
- ❖ ***ENG.10 - System Testing***

www.automotivespice.com

Typical result from an Automotive SPICE Gap Analysis in a generic Small Setting on an average project (Not "Best in class").



GAP ANALYSIS RATING

rating			
N	P	L	F

ID	Process name	Capability	Implementation Level (L1)	Major Criticality
MAN.03	Project Management	1	60%	Definition, planning of SW activities is poor. Accordingly monitoring is reactive.
SUP.01	Quality Assurance	0	50%	Very low Focus on SW development activities
SUP.08	Configuration Management	0	50%	This process area is not supported adequately in terms of tools.
SUP.09	Problem Resolution Management	1	60%	The activities of these processes are not tracked adequately.
SUP.10	Change Request Management	1	60%	
ENG.02	System Requirements Analysis	1	60%	This process is not systematic and needs strong improvement and tuning (e.g. consolidated list of the system features).
ENG.03	System Architectural Design	1	60%	Some aspects of this process area are mingled with ENG.2.
ENG.04	Software Requirements Analysis	0	50%	The documentation (SW Requirements) is often NOT up-to date and is not an adequate support for the ENG.8.
ENG.05	Software Design	0	50%	Some aspects of this process area are mingled with ENG.4 (SW Description Document). Some important design choices are not documented.
ENG.06	Software Construction	0	50%	SW Verification is conducted effectively but informally and very partially.
ENG.07	Software Integration Testing	0	50%	Formality is <u>very</u> low.
ENG.08	Software Testing	1	60%	Formality is low. Traceability to SW Requirements is poor.
ENG.09	System Integration Testing	0	50%	Formality is <u>very</u> low.
ENG.10	System Testing	1	70%	Formality is low. Traceability to System Requirements is poor
ACQ.04	Supplier Monitoring	Not Applicable		

A lot of stuff to do in order to improve!

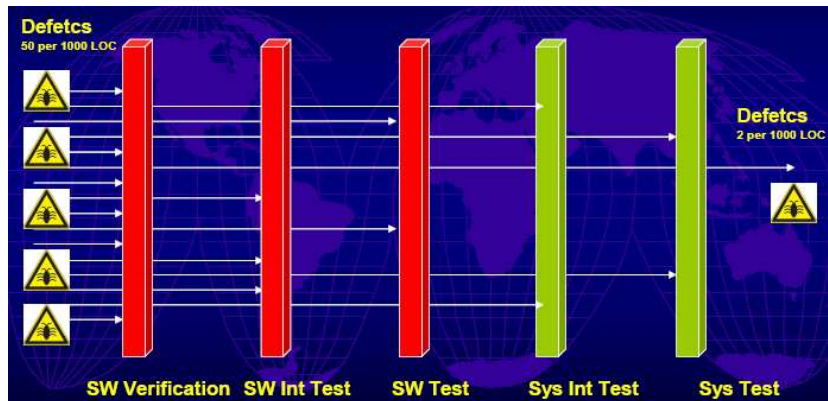
So What Strategies and Techniques?

- ❖ **SW Units Verification**
- ❖ **SW Integration Testing**
- ❖ **SW Testing**
- ❖ **System Integration Testing**
- ❖ **System Testing**



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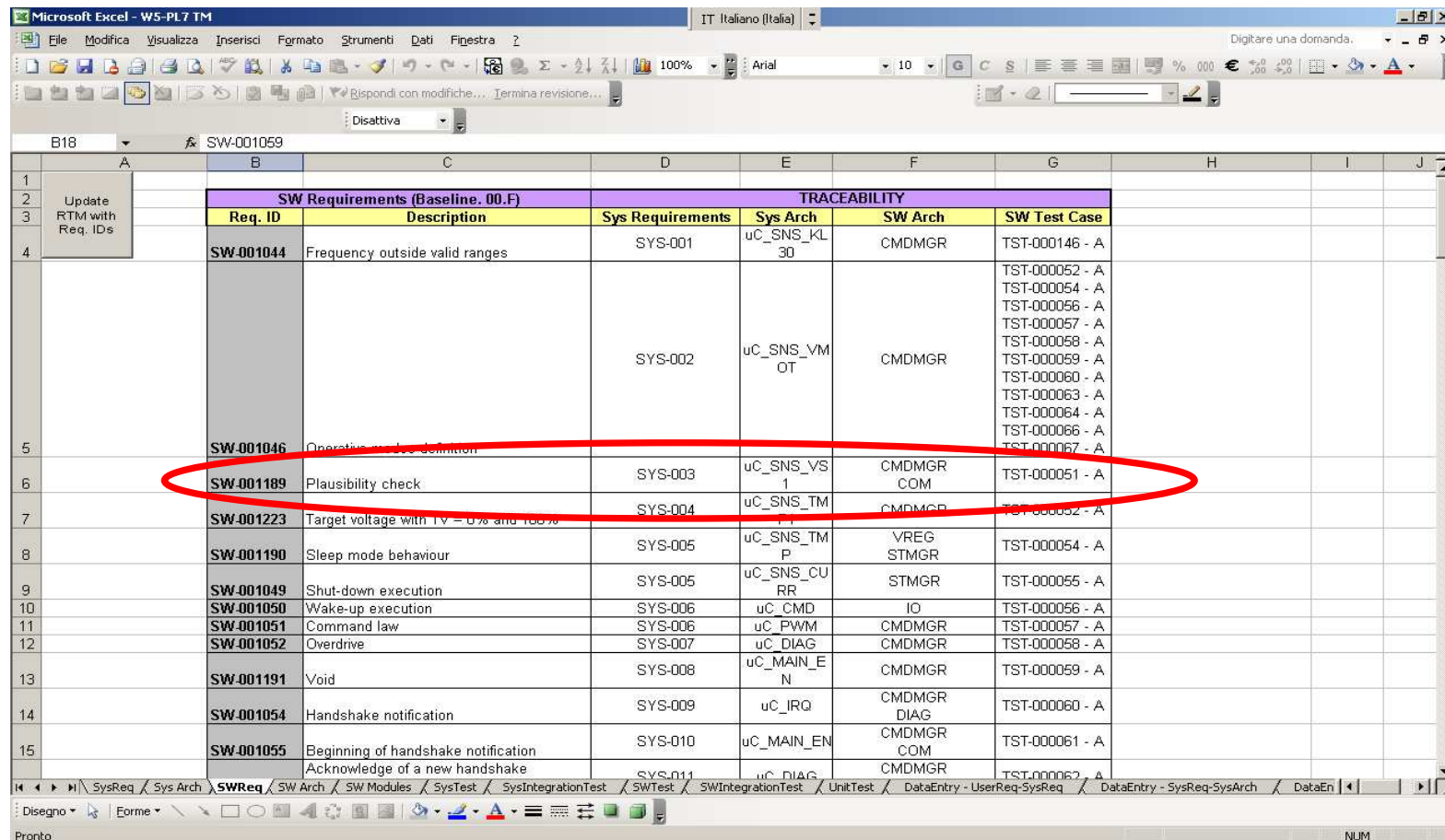
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Setup a light-weight and usable Traceability Infrastructure:

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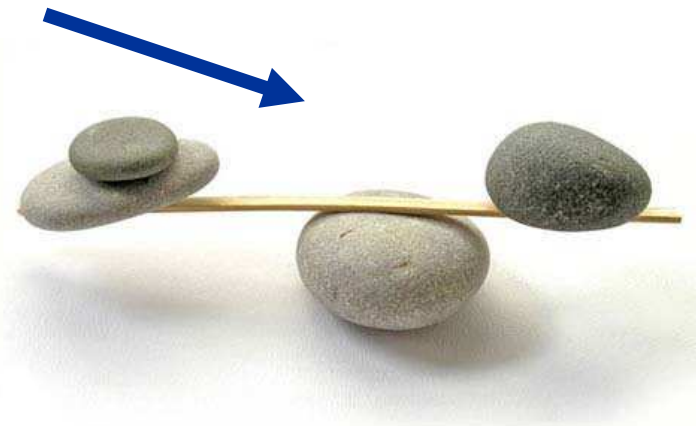
SPICE



SW Requirements (Baseline. 00.F)		TRACEABILITY			
Req. ID	Description	Sys Requirements	Sys Arch	SW Arch	SW Test Case
SW-001044	Frequency outside valid ranges	SYS-001	uC_SNS_KL 30	CMDMGR	TST-000146 - A
SW-001046	Operative mode definition	SYS-002	uC_SNS_VM OT	CMDMGR	TST-000052 - A TST-000054 - A TST-000056 - A TST-000057 - A TST-000058 - A TST-000059 - A TST-000060 - A TST-000063 - A TST-000064 - A TST-000066 - A TST-000067 - A
SW-001189	Plausibility check	SYS-003	uC_SNS_VS 1	CMDMGR COM	TST-000051 - A
SW-001223	Target voltage with TV = 0% and 100%	SYS-004	uC_SNS_TM P	CMDMGR	TST-000052 - A
SW-001190	Sleep mode behaviour	SYS-005	uC_SNS_TM P	VREG STMGR	TST-000054 - A
SW-001049	Shut-down execution	SYS-005	uC_SNS_CU RR	STMGR	TST-000055 - A
SW-001050	Wake-up execution	SYS-006	uC_CMD	IO	TST-000056 - A
SW-001051	Command law	SYS-006	uC_PWM	CMDMGR	TST-000057 - A
SW-001052	Overdrive	SYS-007	uC_DIAG	CMDMGR	TST-000058 - A
SW-001191	Void	SYS-008	uC_MAIN_E N	CMDMGR	TST-000059 - A
SW-001054	Handshake notification	SYS-009	uC_IRQ	CMDMGR DIAG	TST-000060 - A
SW-001055	Beginning of handshake notification Acknowledge of a new handshake	SYS-010	uC_MAIN_EN	CMDMGR COM	TST-000061 - A
		SYS-011	uC_DIAG	CMDMGR	TST-000062 - A

Focus on Balanced Strategies

- ❖ **SW Units Verification**
- ❖ **SW Integration Testing**
- ❖ **SW Testing**
- ❖ **System Integration Testing**
- ❖ **System Testing**



It is strongly advisable to devise jointly the overall V&V strategy to optimize the effort and to compensate weaknesses of each V&V layer!

Aggregate Activities not Strategies

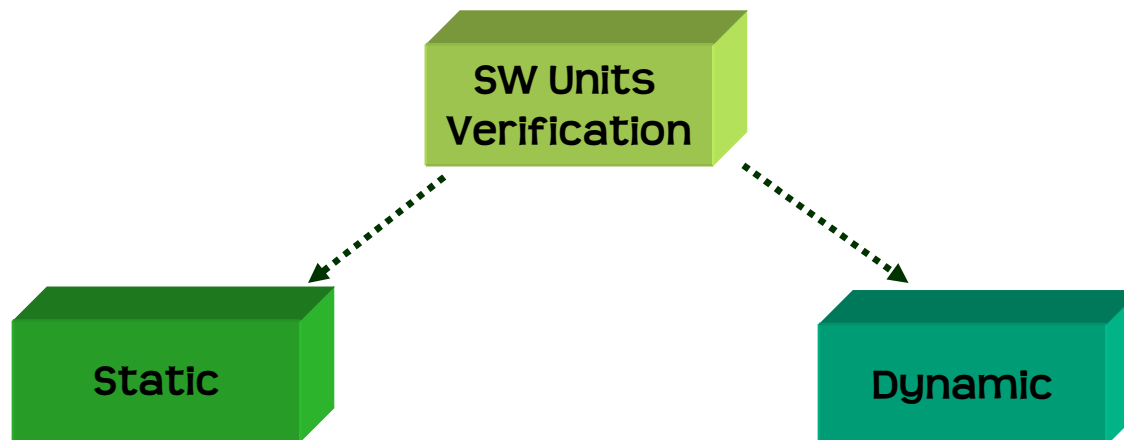
When V&V strategies and objectives are clear, activities can be conveniently and effectively combined.

A typical opportunity is:

- ***SW Integration Testing***
- ***SW Testing***



ENG.6 – SW Units Verification



Unit Testing is made difficult by:

- ❖ ***HW constraints do not encourage unit test on host (8bit, 16bit, flash)***
- ❖ ***Very time consuming***
- ❖ ***Poor documentation at SW Design level***

ENG.6 – SW Unit Verification (2)

Full speed ahead on:

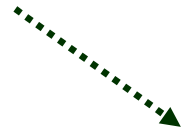
- ❖ ***Functional Code Inspection***
- ❖ ***Enforcement of defined Corporate Coding Standard***
- ❖ ***Respect of well selected SW Metrics***
- ❖ ***Respect of MISRA C 2004 Rules***



Static Analysis techniques play a key role, especially in increasing small size software quality!

Functional Code Inspection

❖ *Functional Code Inspections*



Pair programming

- ❖ *Driver and Navigator working together on one task***
- ❖ *Roles changing often***
- ❖ *Collective responsibility for outcome***
- ❖ *Bringing together of multiple perspectives, experiences, abilities, and expertise***



Effective UT approaches may include:

- ❖ *Performing Unit Testing of selected **critical functions** on emulator depending on the application and its context*
- ❖ *Performing Unit Testing of selected functions **algorithmic intensive** on Host*
- ❖ *Performing Unit Testing of selected single lines of code (LOC) – typically complex **mathematical statements** – on Host*
- ❖ *Application of advanced techniques (e.g. pairwise testing)*

Pairwise Testing:

- ❖ **Pairwise testing is an approach to combinatorial testing that executes a pairwise test data set.**
- ❖ **Pairwise test data set - A set of test cases that covers all combinations of the selected test data values for every pair of a system's input variables.**

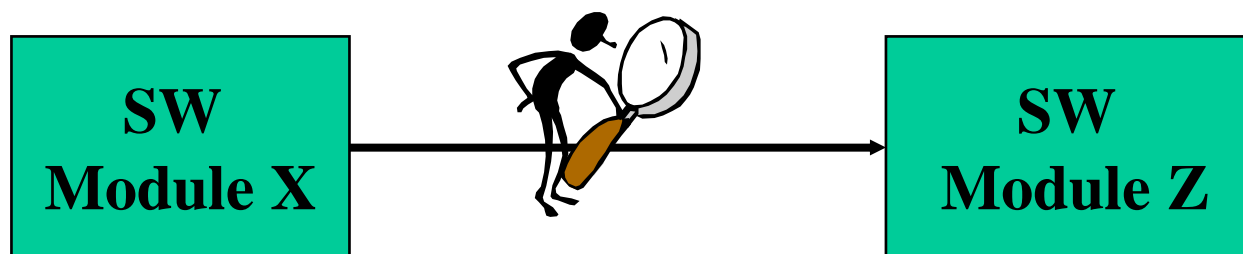
TestNo	DEPENDENTS	MARITAL STATUS	SPOUSE FIRST NAME	EXPECTED RESULT
Test1	No children	Single	Mary	
Test2	No children	Married	Null	
Test3	No children	Divorced	Greater than 20 Characters	
Test4	Dependent children	Single	Null	Bug is detected in Test 2
Test5	Dependent children	Married	Greater than 20 characters	
Test6	Dependent children	Divorced	Mary	
Test7	Adult children	Single	Greater than 20 characters	
Test8	Adult children	Married	Mary	
Test9	Adult children	Divorced	Null	

only 9 test cases instead of 27!

ENG.7 - SW Integration Test (3)

Focusing on SW Integration Test is made difficult by:

- ❖ ***"What is SW Integration Testing?"***
- ❖ ***Poor documentation at SW Design level***
- ❖ ***Heavy usage of global variables***



ENG.7 - SW Integration Test (2)

Effective approach may include combination of:

- ❖ ***Smoke Testing (based on functional testing)***
- ❖ ***Perform SW Integration Testing to verify that the CCR (Critical Computer Resources) usage is within defined thresholds....***

Table 13 — Methods for software integration testing

Methods		ASIL			
		A	B	C	D
1a	Requirements-based test ^a	++	++	++	++
1b	Interface test	++	++	++	++
1c	Fault injection test ^b	+	+	++	++
1d	Resource usage test ^{c, d}	+	+	+	++
1e	Back-to-back comparison test between model and code, if applicable ^e	+	+	++	++

SW Testing is made difficult by:

- ❖ ***Poor SW Requirement Specification***
- ❖ ***Missing Traceability Data***
- ❖ ***Not enough Time or Resources***



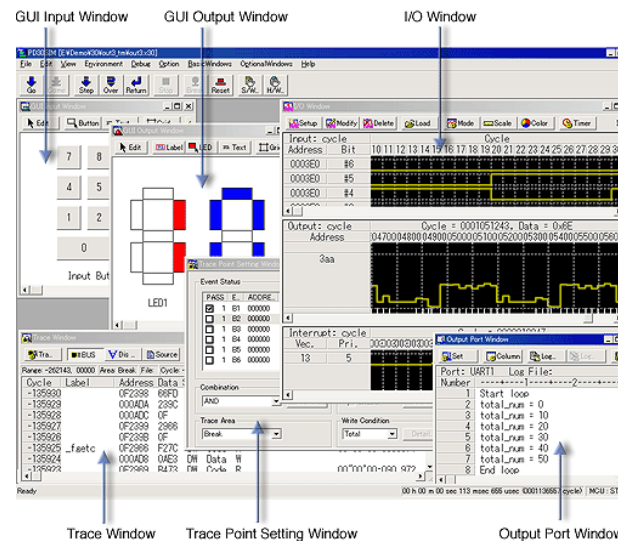
A viable approach for SW Testing is:

- ❖ ***First of all Improve SW Requirements Specification Quality***
- ❖ ***Then, pursue a Requirement-based Testing approach***
 - ***Trade-off in the specification quality of SW test cases is acceptable***
 - ***Some Automation may be possible depending on the environment - IDEs often allow to collect source code coverage measures***
 - ***Grey-Box Testing: Using an emulator it is typically possible to monitor specific points in SW (e.g. SW interfaces, global variables, registers)***

ENG.8 - SW Testing (3)

For some applications SW Testing can also be executed (and even automatized) conveniently on the Simulator offered by IDE

❖ For example it can be possible to setup a GUI to stimulate the software



The drawback is that the environment is less representative!

Focusing on System Integration Test is difficult because:

- ❖ ***"What is System Integration Testing?"***
- ❖ ***Poor System Architectural Design***



Effective approach may include:

❖ Focus on Interface Testing

- ***Focus on HW-SW Interface (HSI) Testing***

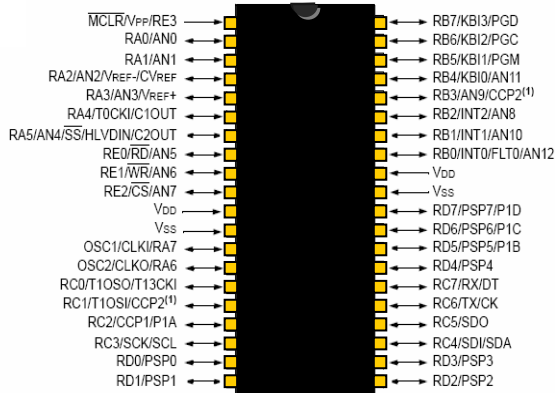


- ***Rely on System Testing and prototypes assembling to complement HSI Testing (HW-MEC, MEC-MEC)***

Effective HW-SW Interface Testing needs a HSI detailed specification:

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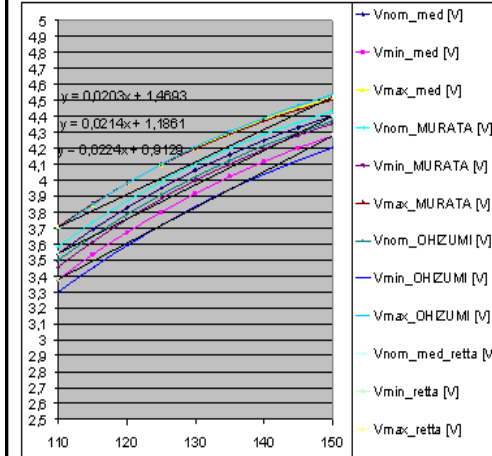
SPICE



- Port A – Digital I/O and Analogue in ...
- Port B – Digital I/O and Analogue in ...
- Port C – Serial (RS-232 / SPI/ I2C) ...
- Port D – Digital I/O ...
- Port E – More Analogue

5.2.5.1.5 uC_SNS_TMP1

Temperature voltage signal generated by the resistor divider including the temperature sensor NTC2. uC_SNS_TMP1 is a signal that depends on the temperature with the following law:



The nominal formula $y=0.0214x+1.1861$ shall be implemented in the SW.

The HW/SW uC_SNS_TMP1 interface is:

1. Analog Signal
2. Operating range: 1 to 3.0V
3. Worst case range: $V_{SS}-0.3V$ to $V_{DD}+0.3V$
4. Offset ground level: 0mV (theoretical) at 25°C
5. Offset drift from 0µV/°C
6. Voltage Conversion de-rating: T.b.verified
7. Signal frequency range: equivalent to a time constant of tbd ms

System Test is made difficult by:

- ❖ ***Designing and engineering effective system test cases (sometimes can be highly complex)***
- ❖ ***Poor System Requirements Specification***
- ❖ ***Time Consuming***



Standard system test activity is usually performed by a dedicated group (e.g. LABORATORIO) to test the system in its operating environment

Effective approach to System Testing may include combination of:

- ❖ ***Coordination with System Engineers***

Emphasis on Requirement Verification Criteria

- ❖ ***Synergy with corporate quality management
(often driven by APQP - Advanced Product Quality
Planning)***

***Balance between Functional and Environmental
Testing***

What about regression test strategies?

Effective and *Sustainable* Regression Testing Strategies may include:

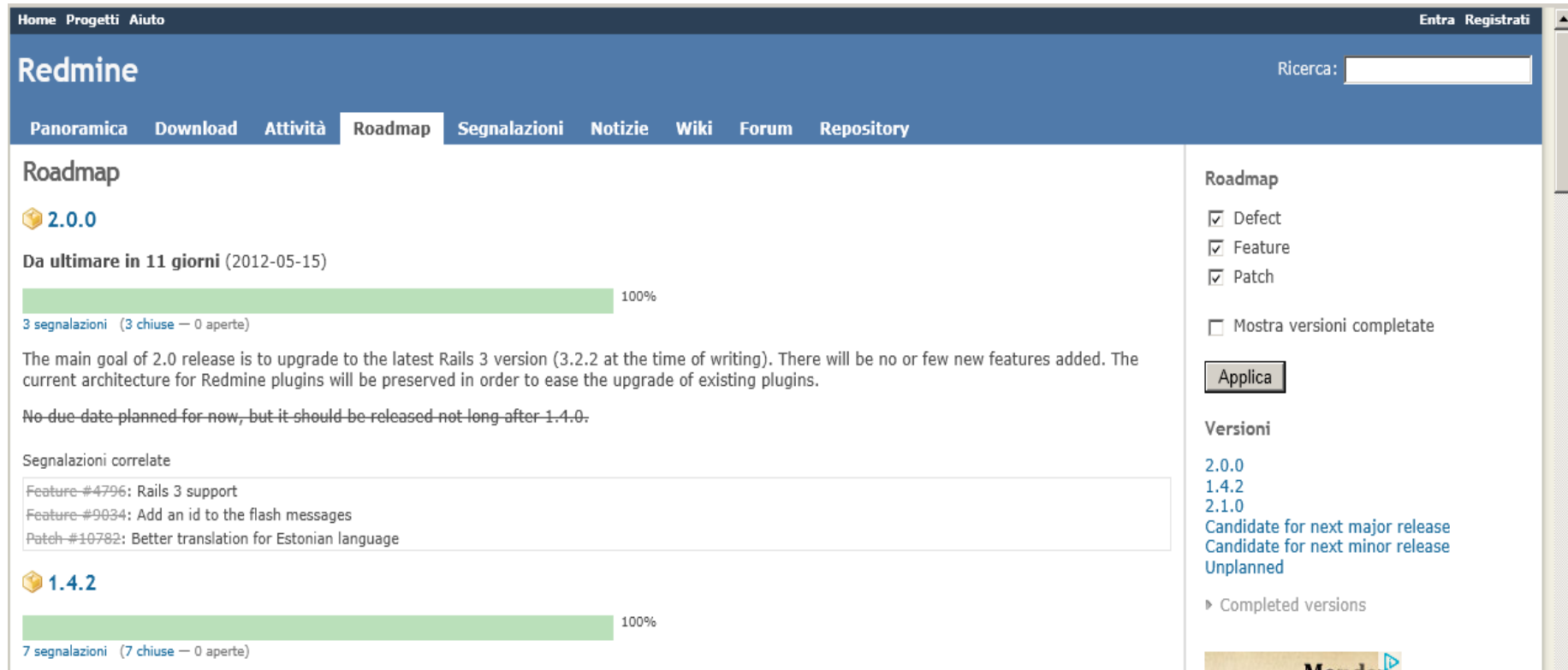
- ❖ ***Selective and Flexible selection criteria***
- ❖ ***Rely on Good Traceability Data***



...and the planning of V&V tasks?

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SPICE



The screenshot shows the Redmine website interface. At the top, there are navigation links: Home, Progetti, Aiuto, and user options: Entra, Registrati. The main header is "Redmine" with a search bar labeled "Ricerca:". Below the header is a navigation menu with items: Panoramica, Download, Attività, Roadmap (selected), Segnalazioni, Notizie, Wiki, Forum, Repository.

The "Roadmap" section is active, showing two versions:

- 2.0.0**: "Da ultimare in 11 giorni (2012-05-15)". A progress bar shows 100% completion. It has 3 segnalazioni (3 chiuse, 0 aperte). The text states: "The main goal of 2.0 release is to upgrade to the latest Rails 3 version (3.2.2 at the time of writing). There will be no or few new features added. The current architecture for Redmine plugins will be preserved in order to ease the upgrade of existing plugins. No due date planned for now, but it should be released not long after 1.4.0." Below this, "Segnalazioni correlate" lists: Feature #4796: Rails 3 support, Feature #9034: Add an id to the flash messages, Patch #10782: Better translation for Estonian language.
- 1.4.2**: A progress bar shows 100% completion. It has 7 segnalazioni (7 chiuse, 0 aperte).

On the right side, there is a "Roadmap" sidebar with checkboxes for Defect, Feature, and Patch (all checked). There is also a checkbox for "Mostra versioni complete" (unchecked) and an "Applica" button. Below that, the "Versioni" section lists 2.0.0, 1.4.2, and 2.1.0. Under 2.1.0, it says "Candidate for next major release" and "Candidate for next minor release". Under 2.0.0, it says "Unplanned". There is a "Completed versions" link and a "Mostra" button with a play icon.

www.redmine.org

The presented strategies have been implemented (partially or fully) in several "small settings" scenarios, very often with satisfactorily results.

THANKS.



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